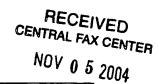
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Inventor(s):

Chyan

Case:

15-6-9

Serial No.:

09/648164

Filing Date:

8/25/2000

Examiner:

Dickey

Group Art Unit:

2826

I hereby certify that this Petition is being faxed to the

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Empus

United States Patent Office on November 5, 2004 to fox number 703-872-9318 addressed to this Commissioner of Patents and Tradernarks Washington, D.C. 20231

Title:

ARCHITECTURE FOR CIRCUIT CONNECTION OF A VERTICAL

TRANSISTOR

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D. C. 20231

SIR:

<u>PETITION FOR EXTENSION OF TIME</u>

Applicant(s) petition(s) the Commissioner of Patents and Trademarks to extend the time for response to the action dated 5/05/04 for <u>3 (Three) months</u> from <u>8/05/04</u> to <u>11/05/04</u>.

Please charge Agere Systems, Inc. Deposit Account No. 501735 in the amount of \$980.00 to cover the cost of the extension. Any deficiency or overpayment of the required fee should be charged or credited to Deposit Account No. 501735 as required to correct the error. Duplicate copies of this petition are enclosed.

Respectfully Submitted,

Ferdinand M. Romano

Reg. 32,752

Attorney for Applicant

Date: 5 November 2004